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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant:	Brian R. Bennett et al.	Examiner:	Denise Tran
Serial No.:	09/470329	Group Art Unit:	2186
Filed:	December 22, 1999	Docket:	884.174US1
Title:	PREVENTION OF LIVE-LOCK IN A MULTI-PROCESSOR SYSTEM		
Assignee:	Intel Corporation	Customer No.	21186

**NOTICE OF APPEAL FROM THE DECISION OF THE EXAMINER  
TO THE BOARD OF PATENT APPEALS AND INTERFERENCES**

**MAIL STOP AF**  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

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In compliance with 37 C.F.R. § 1.191, Applicants hereby appeal to the Board of Patent Appeals and Interferences from the decision dated July 15, 2003 of the Examiner rejecting claims 1-21 of the above-identified patent application.

A request for an extension of time to respond to the Examiner's rejection is submitted herewith along with payment of the required extension fee.

Please charge the amount of \$330.00 in payment of the Notice of Appeal fee under 37 C.F.R. § 1.17(b), as well as any additional required fees, to Deposit Account No. 19-0743.

Respectfully submitted,  
  
BRIAN R. BENNETT ET AL.

By Applicants' Attorneys,  
  
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Date Jan. 14, 2004 By Ann M. McCrackin  
Ann M. McCrackin  
Reg. No. 42,858

**CERTIFICATE UNDER 37 C.F.R. 1.8:**  
I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to MAIL STOP AF, Commissioner of Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on January 14, 2004.

Anne M. Richards Anne M. Richards  
Name Signature